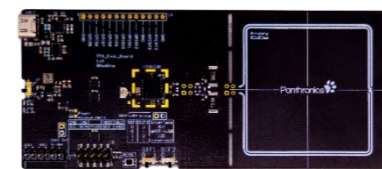


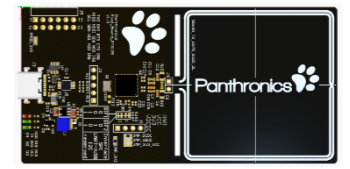
NFC READER SOLUTIONS	Released	Released	Release in April 2023
PRODUCT	PTX100R	PTX105R	PTX130R
Product description	High-performance, high-power, multi-protocol NFC Forum reader. Universal SW device integration.	Mid-power, multi-protocol NFC Forum compliant reader. Universal SW device integration.	High-performance, high-efficiency and high-power multi-protocol NFC Forum compliant reader. Universal SW device integration. Additional applications and new Form-Factors.
Technology	180nm	180nm	180nm
Standards & Protocols			
<b>NFC Forum certification, FeliCa pre-certification test</b>	NFC Forum compliance, ✓	NFC Forum compliance, -	NFC Forum compliance, -
<b>Reader / Writer</b>	ISO/IEC 14443 A/B, EMVCo 3.0/3.1 ISO/IEC 18092, FeliCa™ ISO/IEC 15693 ISO/IEC 18000-3 M1	ISO/IEC 14443 A/B, EMVCo 3.1 ISO/IEC 18092, FeliCa™ ISO/IEC 15693 ISO/IEC 18000-3 M1	ISO/IEC 14443 A/B, EMVCo 3.1 ISO/IEC 18092, FeliCa™ ISO/IEC 15693 ISO/IEC 18000-3 M1
Carrier frequency [MHz]	13,56 MHz	13,56 MHz	13,56 MHz
NFC Forum Tag-Type support	2, 3, 4, 5	2, 3, 4, 5	2, 3, 4, 5
ISO/IEC 14443 bitrate [kbit/s]	106/212/424/848 kbit/s	106/212/424/848 kbit/s	106/212/424/848 kbit/s
ISO/IEC 18092 / FeliCa™ <sup>1</sup> , bitrate [kbit/s]	212/424 kbit/s	212/424 kbit/s	212/424 kbit/s
MIFARE Classic® <sup>2</sup> CRYPTO 1 support (MFCC)	✓	✓	✓
ISO/IEC 15693 bitrate [kbit/s]	26.5 kbit/s	26.5 kbit/s	26.5 kbit/s
ISO/IEC 18000-3 M1	✓	✓	✓
EMVCo compliance	✓ (3.1)	✓ (3.1)	✓ (3.1)
EMVCo High Power behind Display Compliance (HPDC)	✓	-	✓
<b>Card emulation (HCE)</b>	✓	✓	✓
NFC Tag-Type emulation @ bitrate [kbit/s]	4A @ [106 kbit/s]	4A @ [106 kbit/s]	4A @ [106 kbit/s]
<b>Peer-to-peer (ISO/IEC 18092)</b>	✓	✓	✓
Passive communication	Initiator	Initiator	Initiator
Active communication	-	-	-
<b>Special Protocols &amp; Features</b>	China ID	China ID	China ID
<b>Product features</b>			
Ultra-low power on-chip MCU with integrated Firmware	✓	✓	✓
High power digital conversion sine wave RF frontend	✓	✓	✓
Digital dynamic power control (DDPC)	✓	✓	✓
DIRAC®: EMI filter-less solution	✓	✓	✓
Advanced Digital Wave-Shaping	✓	✓	✓
Phase Accurate Active Load-Modulation HCE	✓	✓	✓
High receiver/LMA sensitivity [dBc]	-80 dBc	-80 dBc	-80 dBc
Very High Dynamic Range Receiver (VHRR)	✓	✓	✓
Operating distance up to [mm], ISO 14443 <sup>3</sup> / ISO 15693 <sup>4</sup>	140 mm / 320 mm	100 mm / 200 mm	140 mm / 320 mm
RF transmitter supply voltage [V]	2.5 V – 5.5 V	2.5 V – 5.5 V	2.5 V – 5.5 V
Transmitter supply current, max. [mA]	650 mA	430 mA	650 mA
Power Output, max. [W]	2.0 W	1.0 W	2.0 W
Host interface	SPI, I2C, UART	SPI, I2C, UART	SPI, I2C, UART
Supply voltage host interface [V]	1.8V, 3.3V, 5.0V	1.8V, 3.3V, 5.0V	1.8V, 3.3V, 5.0V
Power-down mode current, typ. [µA]	3 µA	3 µA	3 µA
Standby current with LPFD <sup>5</sup> @ HCE, typ. [µA]	16,5 µA	16,5 µA	16,5 µA
Standby current with LPFD and Host event, typ. [µA]	16,5 µA	16,5 µA	16,5 µA
Low Power Card Detection mode (LPCD), typ. [µA], @ 2 Hz	100 µA	100 µA	100 µA
Customizable GPIO	-	-	✓
Temperature range [°C]	-40 to +70	-40 to +70	-40 to +70
Field-detection signal output	IRQ	IRQ	IRQ
<b>Product support and ordering information</b>			
Product packages	QFN56	QFN56	QFN56
Product type	PTX100RDQ56	PTX105RDQ56	PTX130RDQ56
Order code single tray (dry pack)	PTX100RDQ56B	PTX105RDQ56B	PTX130RDQ56B
Order code reel (TR dry reel 7")	PTX100RDQ56D7	PTX105RDQ56D7	PTX130RDQ56D7
<b>Evaluation boards</b>			
Name of evaluation kit	PTX100R NFC Reader Eval Kit	PTX105R NFC Reader Eval Kit	PTX130R NFC Reader Eval Kit
Order number of evaluation kit	10009100	10009105	10009130
NFC IoT PTX105R Pmod™ Board (Renesas Quick Connect IoT)	-	<a href="#">Get your QC PTX105R Pmod board</a>	-
<b>Software / SDKs / GUI</b>	NFC reader libraries (SDK's) for PoS (EMVCo 3.1) and IoT in Non-OS (MCU's, RTOS) and OS versions (Linux, Multi-Tasking-OS). Config Tool GUI for evaluation of IC features, RF optimization and Tag reading (Windows® and Linux)	NFC reader libraries (SDK's) for PoS (EMVCo 3.1) and IoT in Non-OS (MCU's, RTOS) and OS versions (Linux, Multi-Tasking-OS). Config Tool GUI for evaluation of IC features, RF optimization and Tag reading (Windows® and Linux)	NFC reader libraries (SDK's) for PoS (EMVCo 3.1), IoT in Non-OS (MCU's, RTOS) and OS versions (Linux, Multi-Tasking-OS). NCI 2.0 SDK. AIS (Android™ <sup>6</sup> Integration Stack for Android 12). Config Tool GUI for evaluation of IC features, RF optimization and Tag reading (Windows® and Linux)

EVALUATION KITS	NFC READERS	
Evaluation Kit (Order number)	PTX100R NFC Reader Eval Kit (10009100)	PTX105R NFC Reader Eval Kits (10009105) NFC IoT PTX105R Pmod™ Board: <a href="#">Get your QC PTX105R Pmod board and via Renesas RA partner ecosystem</a>
Supported products	PTX100R	PTX105R
Contents (Order number of boards)	<ul style="list-style-type: none"> <li>1 PTX100R EB v1.4 (10009001)</li> <li>1 PTX H-Field Detector Card v1.0 (10009006)</li> <li>1 USB-A to USB-C cable</li> </ul>	<ul style="list-style-type: none"> <li>1 PTX105R EB v1.0 (10009018)</li> <li>1 PTX H-Field Detector Card v1.0 (10009006)</li> <li>1 USB-A to USB-C cable</li> </ul>
Key features	<ul style="list-style-type: none"> <li>High-power (2W), high-performance NFC Reader kit for universal reader hardware platform IC PTX100R. EMVCo L1 with antenna behind display.</li> <li>Easy-to-use, ready-to-go SW integration into any terminal/device Host-MCU architecture supporting all types of NFC protocols, standards, and Tag protocols (ISO14443 A/B, ISO18092, ISO15693, FeliCa and others). EMVCo L1 compliant protocols (PoS SDK).</li> <li>NFC P2P (initiator) and Card Emulation (HCE)</li> <li>Digital dynamic power control (DDPC), DIRAC®: EMI filter-less solution, Advanced Digital Wave-Shaping, Phase Accurate Active Load-Modulation in Card Mode, Very High Dynamic Range Receiver (VHRR)</li> <li>RF-design supported with Config Tool and SDKs</li> </ul>	<ul style="list-style-type: none"> <li>Mid-power (1W) NFC Reader kit for universal reader hardware platform IC PTX105R.</li> <li>Easy-to-use, ready-to-go SW integration into any terminal/device host MCU architecture supporting all types of NFC protocols, standards, and Tag protocols (ISO14443 A/B, ISO18092, ISO15693, FeliCa and others). EMVCo L1 compliant protocols (PoS SDK).</li> <li>NFC P2P (initiator) and Card Emulation (HCE)</li> <li>Digital dynamic power control (DDPC), DIRAC®: EMI filter-less solution, Advanced Digital Wave-Shaping, Phase Accurate Active Load-Modulation in Card Mode, Very High Dynamic Range Receiver (VHRR)</li> <li>RF-design supported with Config Tool and SDKs</li> </ul>
Certifications	FCC & CE compliant, MIC Japan compliance (TELEC labs), SONY FeliCa® pre-test passed	Planned: CE, FCC
Software and tools	<ul style="list-style-type: none"> <li>SDKs "Non-OS" optimized Software solution for integration with any Host-MCU/RTOS. High performance APIs in C source code with compact code size: <ul style="list-style-type: none"> <li>PoS SDK compliant to EMVCo and NFC standards</li> <li>IoT SDK compliant to all relevant NFC standards</li> </ul> </li> <li>SDKs "OS": PoS (EMVCo compliant) and IoT reader libraries in C source code including extended SW functions for OS's: <ul style="list-style-type: none"> <li>Linux®, RTOS and any Multi-Tasking OS</li> </ul> </li> <li>Config Tool for evaluation and Demo GUI (Windows® and Linux): Demonstration and evaluation of IC-Features, RF/antenna optimization and Tag read (via USB interface)</li> <li>"Tunneling"-SDK: UART to SPI bridge in C-source code for any MCU providing direct connection of the Config Tool to the PTX-IC to optimize RF/coil design and RF parameters.</li> <li>Antenna design support with open-source tool Qucs Studio.</li> </ul>	<ul style="list-style-type: none"> <li>SDKs "Non-OS" optimized Software solution for integration with any Host-MCU/RTOS. High performance APIs in C source code with compact code size: <ul style="list-style-type: none"> <li>PoS SDK compliant to EMVCo and NFC standards</li> <li>IoT SDK compliant to all relevant NFC standards</li> </ul> </li> <li>SDKs "OS": PoS (EMVCo compliant) and IoT reader libraries in C source code including extended SW functions for OS's: <ul style="list-style-type: none"> <li>Linux®, RTOS and any Multi-Tasking OS</li> </ul> </li> <li>Config Tool for evaluation and Demo GUI (Windows® and Linux): Demonstration and evaluation of IC-Features, RF/antenna optimization and Tag read (via USB interface)</li> <li>"Tunneling"-SDK: UART to SPI bridge in C-source code for any MCU providing direct connection of the Config Tool to the PTX-IC to optimize RF/coil design and RF parameters.</li> <li>Antenna design support with open-source tool Qucs Studio.</li> </ul>
Target applications	High-power universal multi-market reader solutions: <ul style="list-style-type: none"> <li>EMVCo POS, mPOS and SmartPOS terminals (antenna behind display)</li> <li>Compact POS Chip&amp;Sign, ATM POS modules</li> <li>Transportation terminals, gateways, industrial application</li> <li>High-end access control, door locks</li> <li>Accessories identification, brand protection, printing devices</li> <li>Gaming consoles, fitness accessories and Smart-TV</li> <li>e-Government, eMRTD, home eID</li> <li>Small FormFactor NFC Reader (e.g.: Dongle, medical, pen-reader)</li> </ul>	Mid-power universal multi-market reader solutions: <ul style="list-style-type: none"> <li>EMVCo POS terminals</li> <li>Transportation terminals, gateways, industrial application</li> <li>Access control, door locks</li> <li>Accessories identification, brand protection, printing devices</li> <li>Gaming consoles, fitness accessories and Smart-TV</li> <li>e-Government, eMRTD, home eID</li> <li>Small FormFactor NFC Reader (e.g.: Dongle, medical, pen-reader)</li> </ul>
Application team support (Registered customers)	<ul style="list-style-type: none"> <li>SW-expert team supports you with target system Software/Firmware integration</li> <li>Expert support for RF and antenna design for your end application <ul style="list-style-type: none"> <li>Retrofit support: Customer antenna retrofitted with customer antenna</li> <li>End form factor RF optimization and verification with "Tunneling"-SDKs</li> </ul> </li> <li>EMVCo L1 certification support</li> </ul>	<ul style="list-style-type: none"> <li>SW-expert team supports you with target system Software/Firmware integration</li> <li>Expert support for RF and antenna design for your end application <ul style="list-style-type: none"> <li>Retrofit support: Customer antenna retrofitted with customer antenna</li> <li>End form factor RF optimization and verification with "Tunneling"-SDKs</li> </ul> </li> <li>EMVCo L1 certification support</li> </ul>

For registration, ordering of boards and SDK's please contact [sales@panthronics.com](mailto:sales@panthronics.com)



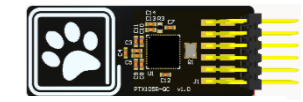
PTX100R EB v1.4



PTX105R EB v1.0



PTX Field Detector Card v1.0



NFC IoT PTX105R Pmod™ Board

<sup>1</sup> FeliCa™ is a registered trademark of Sony Group Corporation  
<sup>2</sup> MIFARE Classic® is a registered trademark of NXP B.V.  
<sup>3</sup> 6,5 x 6,5 cm coil @ 3,5V supply voltage  
<sup>4</sup> 19 x 19 cm coil @ 3,5V supply voltage  
<sup>5</sup> Low Power Field Detection (LPFD)  
<sup>6</sup> Android is the trademark of Google LLC