



**Job:** Digital Design Verification Engineer (m/f/d)  
**Location:** Graz Austria  
**Start Date:** immediately

### Overview

Panthronics AG an independent IC company with expertise in high performance NFC/RFID hardware and software solutions is anticipating the forthcoming launch of the PTX range of differentiated NFC products. We are currently looking for outstanding people in this domain to strengthen our team in Graz namely a **Digital Design Verification Engineer** for the development of the verification components for digital building blocks used ICs for RFID/NFC, Mobiles, Wearables, and IoT-Applications.

This is an exciting opportunity to join a first-class team and gain a meaningful equity position within a fast-growing company.

### Job Description

- Reviewing design specification and selecting the appropriate verification methodology
- Providing verification plans based on design specification including organizing verification plan meetings with design team
- Specification and implementation of block-level testbench for design blocks based on the selected methodology
- Scripting activities for automation of recurring tasks
- Test implementation on existing block and system level testbenches
- Alignment with engineers of other disciplines on interfaces and functionality of IPs
- Support for lab validation

### Job Profile

- Degree in the field of Electronic/Electrical engineering
- Expert knowledge in design verification using Verilog/System Verilog/UVM (5 years+)
- Experience in testbenches design and state-of-the-art verification methodologies (UVM, directed tests, metric-driven verification, formal verification)
- Knowledge of standard SOC components ( $\mu$ -controllers, bus-systems, host-interfaces), NFC-knowledge is a big plus
- Programming language skills for writing test-code and job-automation (Verilog/SV, VHDL, Bash, Perl, Python, Java, Assembler)
- Fluent in English – German skills are plus

### Compensation

- Salary commensurate with market based on individual person  
*This position is in accordance with the Austrian collective agreement for employees within engineering companies, employment group IV: [www.ingenieurbueros.at/verband/de/kollektivvertrag](http://www.ingenieurbueros.at/verband/de/kollektivvertrag).*
- Generous Stock Incentive program
- Variable incentive Scheme

### Contact

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