



Job: Digital Design Engineer (m/f/d)
Location: Graz Austria
Start Date: immediately

Overview

Panthronics AG an independent IC company with expertise in high performance NFC/RFID hardware and software solutions is anticipating the forthcoming launch of the PTX range of differentiated NFC products. We are currently looking for outstanding people in this domain to strengthen our team in Graz namely a **Digital Design Engineer** for the development of the digital building blocks of ICs for RFID/NFC, Mobiles, Wearables, and IoT-Applications.

This is an exciting opportunity to join a first-class team and gain a meaningful equity position within a fast-growing company.

Job Description

- Specification and RTL design of digital components for high-end NFC-controllers
- Integration of sub-blocks and IP into a complex SOC systems
- Performing pre-silicon verification on block and system level
- Definition of timing and power constraint, support for backend engineering
- Alignment with engineers of other disciplines on interfaces and functionality of IPs
- Carrying out of lab validation of digital circuits

Job Profile

- Master's degree in the field of Electronic/Electrical engineering
- Expert knowledge in digital circuit design using Verilog/System Verilog (5 years+)
- Experience in digital design flow and development tools for low power (CPF/UPF)
- Experience in testbenches design and state-of-the-art verification methodologies
- Knowledge of standard SOC components (μ -controllers, bus-systems, host-interfaces), NFC-knowledge is a big plus
- Programming language skills for writing test-code and validation SW (C, Java, Asm)
- Fluent in English – German skill are plus

Compensation

- Salary commensurate with market based on individual person
This position is in accordance with the Austrian collective agreement for employees within engineering companies, employment group IV: www.ingenieurbueros.at/verband/de/kollektivvertrag.
- Generous Stock Incentive program
- Variable incentive Scheme

Contact

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