

Want to do some *real engineering again?*

Job: Senior DSP Engineer (m/f/d)
Location: Graz Austria
Start Date: Q3 2019

Overview

Panthronics AG an independent IC company with expertise in high performance NFC/RFID hardware and software solutions is anticipating the forthcoming launch of the PTX range of differentiated NFC products. We are currently looking for outstanding people in this domain to strengthen our team in Graz. We now require additional experienced **Digital Signal Processing Engineers** for the development of the digital signal processing building blocks of ICs for RFID/NFC, Mobiles, Wearables, and IoT-Applications.

This is an exciting opportunity to join a first-class team and gain a meaningful equity position within a fast-growing company.

Job Description

- Concept development and architecture definition
- Specification, digital design, and validation of sub-blocks required for digital signal processing
- Support specification and development of analog receive and transmit chains
- Interface with system architects, firmware engineers, analog and digital design engineers, and application engineers

Job Profile

- Master's degree in the field of Electronic/Electrical engineering
- At least 5 years' experience in digital communication theory and DSP
- Experience in system development with MATLAB / Octave
- Preferable knowledge of at least one RTL coding language (Verilog, VHDL)
- Preferable knowledge in digital design flow and development tools
- Fluent in English – German skills are a plus

Compensation

- Salary commensurate with market based on individual person
This position is in accordance with the Austrian collective agreement for employees within engineering companies, employment group V: www.ingenieurbueros.at/verband/de/kollektivvertrag.
- Generous Stock Incentive program
- Variable incentive Scheme

Contact

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